

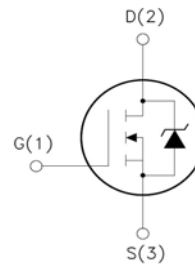
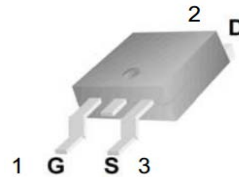
CTKD30N10

100V N-Channel MOSFET

Features:

- Low Intrinsic Capacitances.
- Excellent Switching Characteristics.
- Extended Safe Operating Area.
- Unrivalled Gate Charge : $Q_g = 61.7\text{nC}$ (Typ.).
- $BVDSS = 100\text{V}, I_D = 30\text{A}$
- $R_{DS(on)} : 0.043\Omega$ (Typ) @ $V_G = 10\text{V}$
- 100% Avalanche Tested

TO-252



- 1.Gate (G)
- 2.Drain (D)
- 3.Source (S)

Absolute Maximum Ratings* ($T_c = 25^\circ\text{C}$ Unless otherwise noted)

| Symbol | PARAMETER | Value | Unit |
|--------------|--|---------------------------|------------------|
| V_{DSS} | Drain-Source Voltage | 100 | V |
| I_D | Drain Current | $T_c = 25^\circ\text{C}$ | 30 |
| | | $T_c = 100^\circ\text{C}$ | 21 |
| $V_{GS(TH)}$ | Gate Threshold Voltage | ± 20 | V |
| E_{AS} | Single Pulse Avalanche Energy (note1) | 256 | mJ |
| I_{AR} | Avalanche Current (note2) | 30 | A |
| P_D | Power Dissipation ($T_c = 25^\circ\text{C}$) | 85 | W |
| T_j | Junction Temperature(MAX) | 175 | $^\circ\text{C}$ |
| T_{stg} | Storage Temperature | -55~+175 | $^\circ\text{C}$ |
| TL | Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds | 300 | $^\circ\text{C}$ |

Thermal Characteristics

| Symbol | PARAMETER | Typ. | MAX. | Unit |
|-----------------|---|------|------|---------------------------|
| $R_{\theta JC}$ | Thermal Resistance, Junction to Case | - | 1.8 | $^\circ\text{C}/\text{W}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient | - | 65 | $^\circ\text{C}/\text{W}$ |
| $R_{\theta CS}$ | Thermal Resistance, Case to Sink | - | 110 | $^\circ\text{C}/\text{W}$ |

Electrical Characteristics (T_C=25°C unless otherwise noted)

| Symbol | Parameter | Condition | Min | Typ | Max | Unit |
|---|----------------------------------|--|-----|------|------|------|
| Off Characteristics | | | | | | |
| BV _{DSS} | Drain-Source Breakdown Voltage | V _{GS} =0V, I _D =250μA | 100 | - | - | V |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} =100V, V _{GS} =0V | - | - | 1 | μA |
| I _{GSS} | Gate-Body Leakage Current | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| On Characteristics (Note 3) | | | | | | |
| V _{GS(th)} | Gate Threshold Voltage | V _{DS} =V _{GS} , I _D =250μA | 1.3 | 1.8 | 2.5 | V |
| R _{DS(ON)} | Drain-Source On-State Resistance | V _{GS} =10V, I _D =20A | - | 43 | 46 | mΩ |
| g _{FS} | Forward Transconductance | V _{DS} =5V, I _D =10A | - | 15 | - | S |
| Dynamic Characteristics (Note 4) | | | | | | |
| C _{iss} | Input Capacitance | V _{DS} =50V, V _{GS} =0V, F=1.0MHz | - | 2356 | - | PF |
| C _{oss} | Output Capacitance | | - | 94 | - | PF |
| C _{riss} | Reverse Transfer Capacitance | | - | 83.3 | - | PF |
| Switching Characteristics (Note 4) | | | | | | |
| t _{d(on)} | Turn-on Delay Time | V _{DD} =50V, R _L =5Ω V _{GS} =10V, R _{GEN} =3Ω | - | 7 | - | nS |
| t _r | Turn-on Rise Time | | - | 7 | - | nS |
| t _{d(off)} | Turn-Off Delay Time | | - | 29 | - | nS |
| t _f | Turn-Off Fall Time | | - | 7 | - | nS |
| Q _g | Total Gate Charge | V _{DS} =50V, I _D =10A, V _{GS} =10V | - | 61.7 | - | nC |
| Q _{gs} | Gate-Source Charge | | - | 8.3 | - | nC |
| Q _{gd} | Gate-Drain Charge | | - | 16.7 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| V _{SD} | Diode Forward Voltage (Note 3) | V _{GS} =0V, I _S =10A | - | - | 1.2 | V |
| I _S | Diode Forward Current (Note 2) | - | - | - | 30 | A |
| t _{rr} | Reverse Recovery Time | T _J = 25°C, I _F = 10A | - | 32 | - | nS |
| Q _{rr} | Reverse Recovery Charge | di/dt = 100A/μs (Note 3) | - | 53 | - | nC |
| t _{on} | Forward Turn-On Time | Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD) | | | | |

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS Condition : T_j=25°C, V_{DD}=50V, V_G=10V, L=0.5mH, R_g=25Ω, I_{AS}=32A

Typical Characteristics

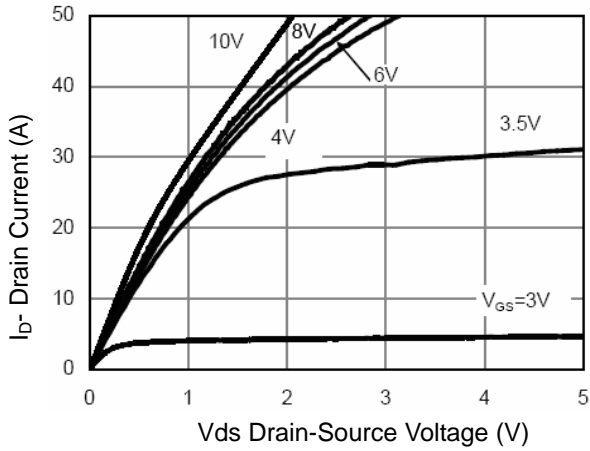


Figure 1 Output Characteristics

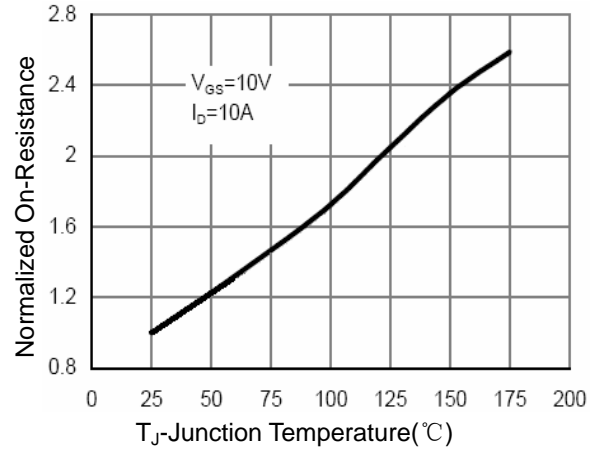


Figure 4 R_{dson} -Junction Temperature

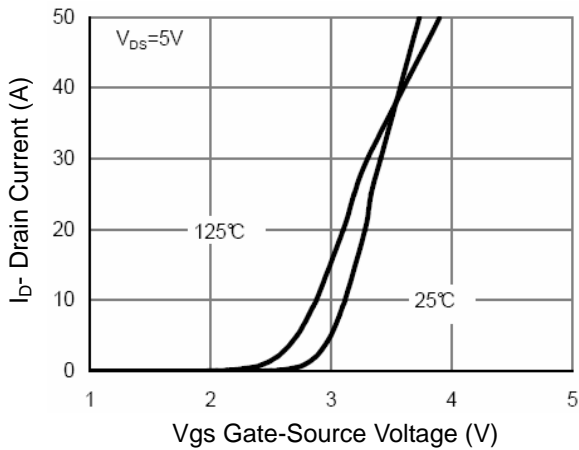


Figure 2 Transfer Characteristics

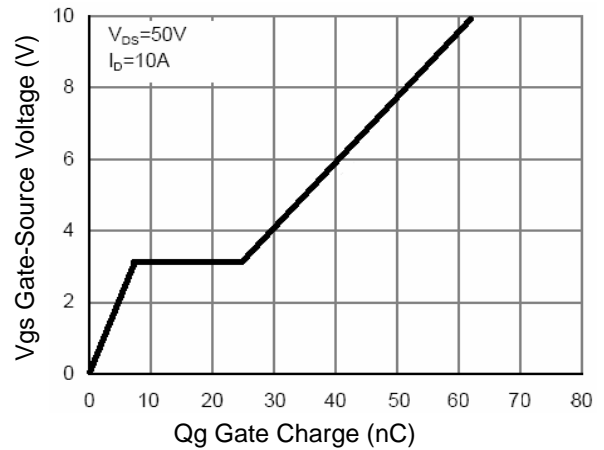


Figure 5 Gate Charge

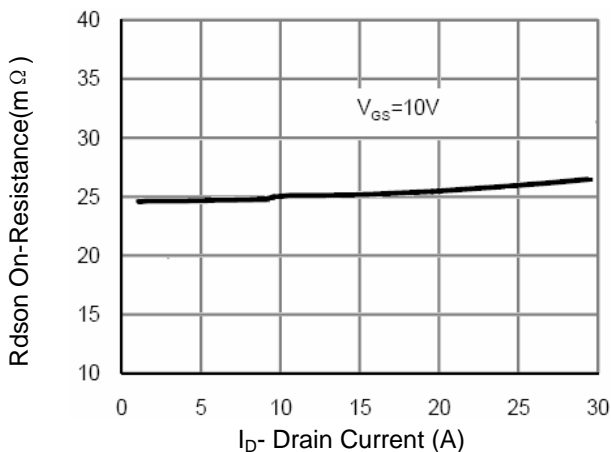


Figure 3 R_{dson} - Drain Current

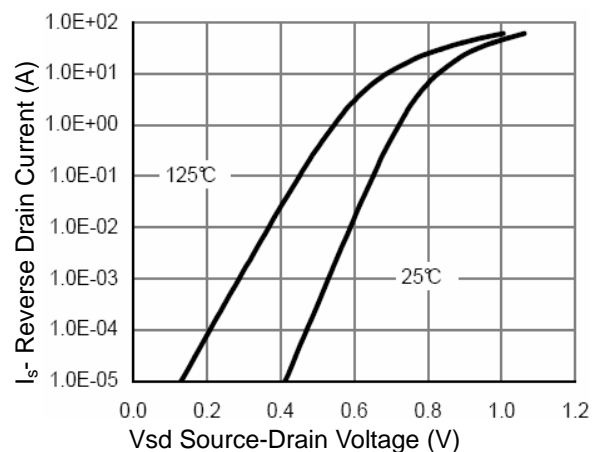


Figure 6 Source- Drain Diode Forward

Typical Characteristics (Continued)

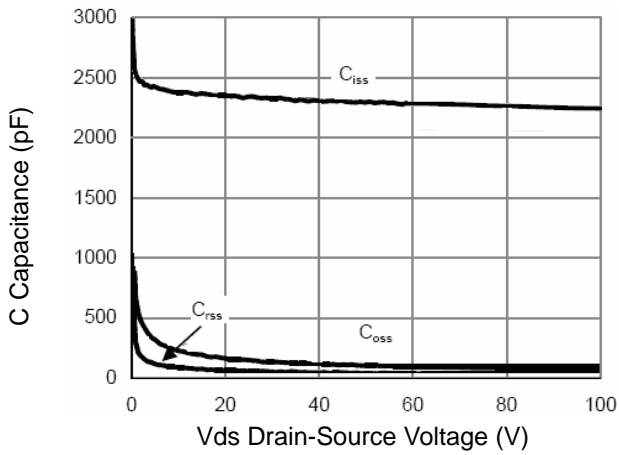


Figure 7 Capacitance vs Vds

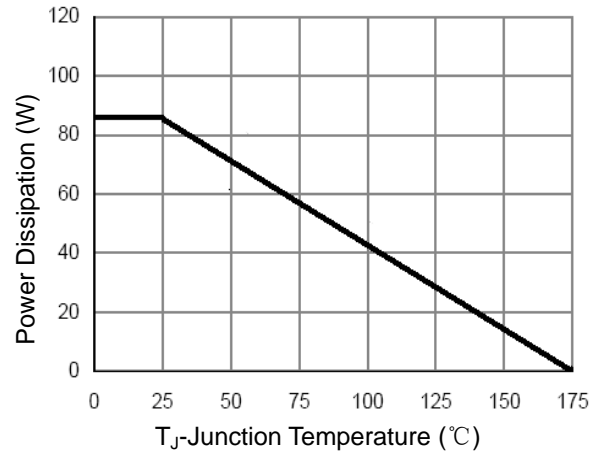


Figure 9 Power De-rating

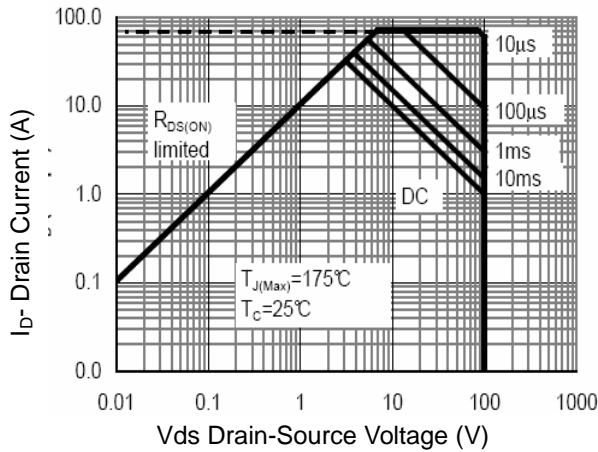


Figure 8 Safe Operation Area

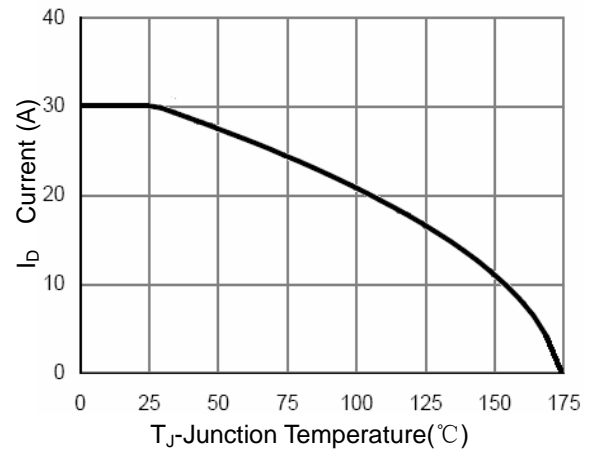


Figure 10 Id Current- Junction Temperature

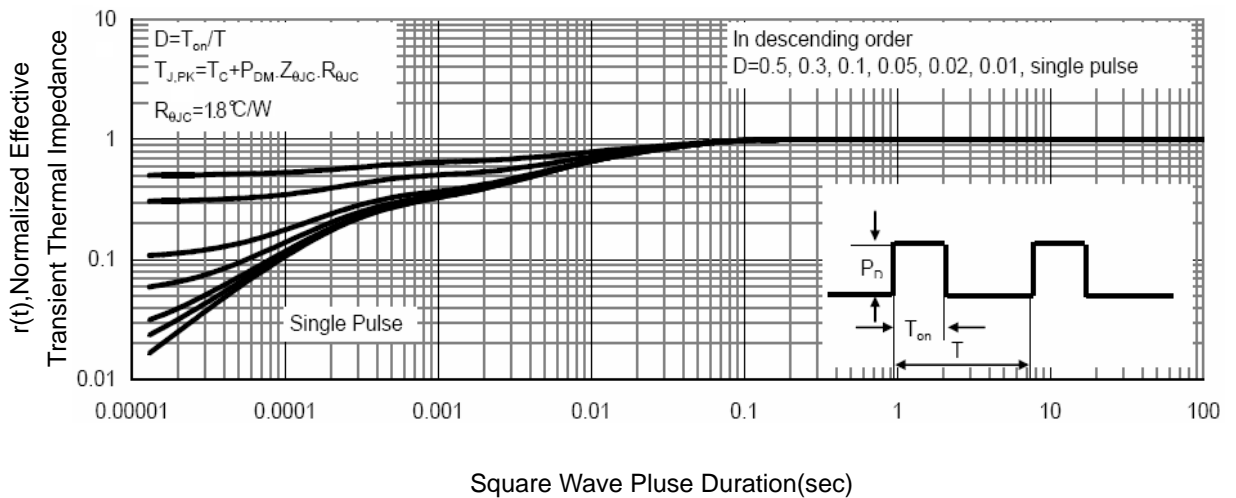
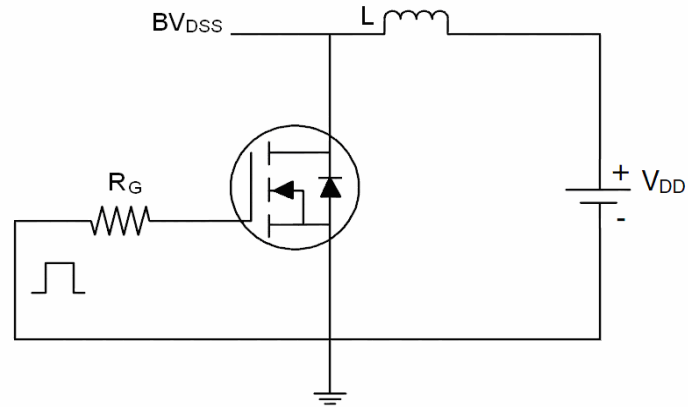


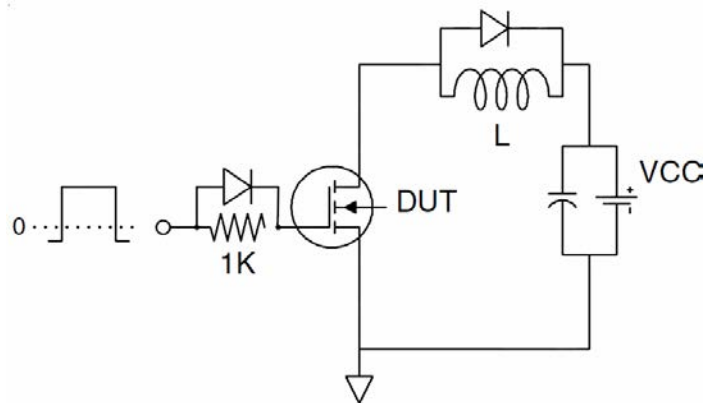
Figure 11 Normalized Maximum Transient Thermal Impedance

Test Circuit

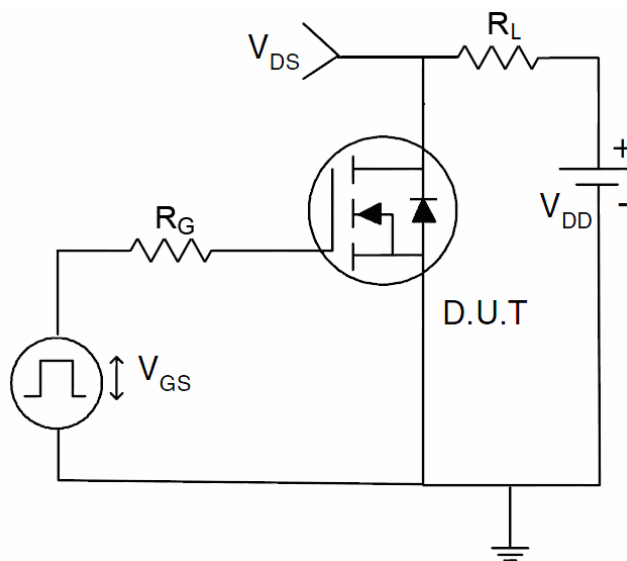
1) A_S Test Circuit



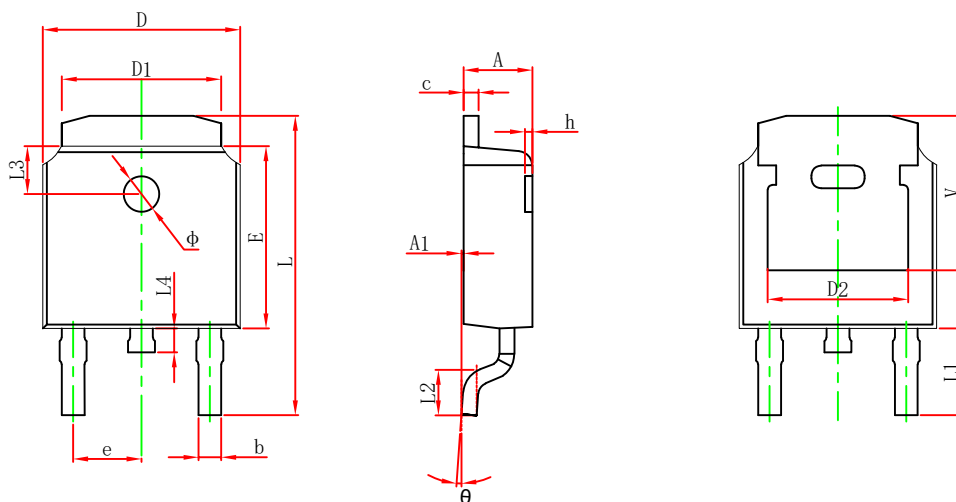
2) Gate Charge Test Circuit



3) Switch Time Test Circuit

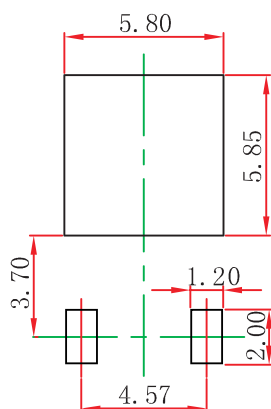


Package Dimension



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|--------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 2.200 | 2.400 | 0.087 | 0.094 |
| A1 | 0.000 | 0.127 | 0.000 | 0.005 |
| b | 0.700 | 0.860 | 0.025 | 0.030 |
| c | 0.460 | 0.580 | 0.018 | 0.023 |
| D | 6.500 | 6.700 | 0.256 | 0.264 |
| D1 | 5.100 | 5.460 | 0.201 | 0.215 |
| D2 | 4.830 REF. | | 0.190 REF. | |
| E | 6.000 | 6.300 | 0.236 | 0.244 |
| e | 2.186 | 2.386 | 0.086 | 0.094 |
| L | 9.712 | 10.312 | 0.382 | 0.406 |
| L1 | 2.900 REF. | | 0.114 REF. | |
| L2 | 1.400 | 1.700 | 0.055 | 0.067 |
| L3 | 1.600 REF. | | 0.063 REF. | |
| L4 | 0.600 | 1.000 | 0.024 | 0.039 |
| Φ | 1.100 | 1.300 | 0.043 | 0.051 |
| θ | 0° | 8° | 0° | 8° |
| h | 0.000 | 0.300 | 0.000 | 0.012 |
| V | 5.250 REF. | | 0.207 REF. | |

TO-252-2L Suggest Pad Layout



NOTE:

1. Controlling dimension: in millimeters.
2. General tolerance: ±0.05mm.
3. The pad layout is for reference purposes only.